


[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

[Advanced Scholar Search](#)
[Scholar Preferences](#)
[Scholar Help](#)

Scholar [All articles](#) - [Recent articles](#) Results 1 - 10 of about 18 for **cyclic redundancy check packe**

All Results

[J Elliott](#)
[M Suzuki](#)

[Method and apparatus for maximizing process throughput - all 3 versions »](#)

JC Elliott - US Patent 5,323,403, 1994 - Google Patents

... The registers are made up of **latches** which are ... identical process genera -tor is a **Cyclic Redundancy Checker (CRC)** ... To accomplish the data integrity **check**, a CRC ...

[Cited by 12](#) - [Related Articles](#) - [Web Search](#)

[Audio transfer system on PHS using error-protected stereo twin VQ - all 3 versions »](#)

K Ikeda, T Mori, T Moriya, N Iwakami, T Kaneko - Consumer Electronics, IEEE Transactions on, 1998 - [ieeexplore.ieee.org](#)

... the transferred **packet**, and then the sender re-sends the **packet**. ... For the second-class bits, the **cyclic redundancy check (CRC) code is calculated** from the second ...

[Cited by 1](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[System and method of error detection for unordered data delivery](#)

FOF INVENTION - [freepatentsonline.com](#)

... An error protection code, such as a **cyclic redundancy check (CRC) code, is calculated** for the data **packet** at the sending node and transmitted to the receiving ...

[Cached](#) - [Web Search](#)

[XX-X XX - all 4 versions »](#)

US Patent 7,103,832 - Google Patents

... by a clock signal CLK 45 applied to the **latches** within each ... 9d method for **cyclic redundancy check** calculation ... providing a W-bit **packet** data slice latch having ...

[Related Articles](#) - [Web Search](#)

[A Fast CRC Update Implementation - all 5 versions »](#)

W Lu, S Wong - Proceedings of the 14th Annual Workshop on Circuits, Systems ... - [ce.et.tudelft.nl](#)

... frame size distributions. Keywords— **Cyclic redundancy check**, parallel CRC calculation, CRC update. I. INTRODUCTION The **cyclic** ...

[Related Articles](#) - [View as HTML](#) - [Web Search](#)

[CRC code calculation circuit and CRC code calculation method - all 3 versions »](#)

M Suzuki, T Inoue, K Mori - US Patent 6,763,495, 2004 - Google Patents

... A CRC (**Cyclic Redundancy Check**) has been conven -tionally ... 12 and 13 respectively **latches** the calculated ... calculation means, a **CRC code is calculated** in parallel ...

[Related Articles](#) - [Web Search](#)

[Method for recovery of frame delineation in HSDPA](#)

N FUKUI - EP Patent 1,353,467, 2003 - [freepatentsonline.com](#)

... a fast access to **packet** services through ... a second binary value, the **CRC code is calculated** according to ... the UE performs a **cyclic redundancy check** according to ...

[Cached](#) - [Web Search](#)

[Cryptographic Hash Functions and Low-Power Techniques for Embedded](#)

Hardware

AL Huang, WT Penzhorn - Industrial Electronics, 2005. ISIE 2005. Proceedings of the ..., 2005 - ieeexplore.ieee.org

... 5) **Latches** and flip ... 1) Cryptographic **Cyclic Redundancy Check (CRC) Cyclic Redundancy**

Check (CRC) is commonly used for ... of code which can be used to **check** if the ...

[Related Articles](#) - [Web Search](#)

A novel communication protocol for wireless short command - all 2 versions »

KF Tsang, LT Lee - Consumer Electronics, IEEE Transactions on, 2003 - ieeexplore.ieee.org

... there is a 2-byte CRC (**Cyclic Redundancy Check**) code to ... and is responsible for error **check** of the ... used to avoid the recipient from receiving duplicated **packet**. ...

[Related Articles](#) - [Web Search](#) - [BL Direct](#)

Two hybrid ARQ algorithms for reliable multicast communications in UMTS networks - all 3 versions »

GD Papadopoulos, G Koltsidas, F Pavlidou - Communications Letters, IEEE, 2006 - ieeexplore.ieee.org

... Under HARQ, besides the **packet** level FEC which is used for error correction, a **cyclic redundancy check (CRC) code is calculated** for every **packet** and included ...

[Related Articles](#) - [Web Search](#)

Google ►

Result Page: 1 2 **Next**

cyclic redundancy check packet OR

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2007 Google

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	301176	(IBM or international adj business adj machine or Mann near2 gregory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/05 09:54
L2	3	1 and (circuit\$4 packet latch CRC XOR subtree and data slice reminder partition).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/05 10:13
L3	83180	(CRC or cyclic near4 redundan\$4 near4 check)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/05 10:09
L4	7	(circuit\$4 packet latch CRC XOR subtree and data slice reminder partition).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/05 10:13
L5	3	4 and calculat\$4 same CRC same remaind\$4 near5 partition	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/05 10:14